

CLAIMS

1. Apparatus for use in a computer system comprising:

a bus architecture;

5 a plurality of modules connected to the bus architecture;

a first arbiter means for controlling initiating transactions on the bus architecture; and

10 a second arbiter means for controlling return transactions on the bus architecture.

2. Apparatus as claimed in claim 1, wherein the bus architecture has separate read, write and transaction control buses, and wherein the first arbiter means controls use of the write and transaction buses and the second
15 arbiter means controls use of the read bus.

3. A computer system comprising apparatus as claimed in claim 1.

4. An integrated circuit comprising apparatus as claimed in claim 1.

20 5. A graphics processing system comprising apparatus as claimed in claim 1.

6. A games console comprising apparatus as claimed in claim 1.